



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2812

Examiner: Whitmore, S.

Applicants: Navaz Lulla et al.

Serial No: 09/689,532

Filing Date: October 12, 2000

For: CIRCUIT FOR GENERATING SILICON ID FOR PLDS

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**DRAWING TRANSMITTAL**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Notice of Allowance mailed December 23, 2004 indicating that formal drawings are due, enclosed are four (4) sheets of formal drawings.

Respectfully submitted,

By: \_\_\_\_\_

Christopher P. Maiorana  
Registration No. 42,829

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Date: March 21, 2005

Attorney Docket No.: 0325.00420

**CERTIFICATE OF MAILING**

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service via First Class Mail in an envelope with sufficient postage and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 21, 2005.

By: Mary Donna Berkley  
Mary Donna Berkley